### **Tutorial-3**

**Step 5 – Test and Refine**

### **5.1 Purpose**

* To confirm that the logic circuit, Boolean expression, and Python code all produce the same, correct output.
* To catch any wiring, logic, or coding mistakes.
* To refine the design for efficiency or clarity if needed.

### **5.2 Testing the Logic Circuit**

1. Open your logic simulator (LogicCircuit app from your brief or Logisim).
2. Build the circuit using:
   * Two 3-input AND gates (one for IGN·DRIV·BELT\_D\_BAR, one for IGN·PASS·BELT\_P\_BAR).
   * One OR gate (to combine the AND outputs).
   * One NOT gate (to make ALARM active-LOW).
3. Label every input and output:
   * Inputs: DRIV, PASS, IGN, BELT\_D\_BAR, BELT\_P\_BAR.
   * Output: ALARM.
4. Test all input combinations:
   * Use the truth table from Step 3.2 as your checklist.
   * For each combination, toggle the switches and check if ALARM matches the table.
5. If a mismatch occurs:
   * Checking gate connections and ensuring correct inputs.
   * Verifying you didn’t confuse BELT\_D\_BAR (1 = unfastened) with belt fastened signals.
   * Ensuring the NOT gate is on the final output and not accidentally inside one of the inputs.

**5.3 Testing the Python Code**

* Running version1.py in your Python environment.
* Input each set of values from the truth table.
* Checking if the printed ALARM matches the truth table.

**5.4 Refining the Solution**

**Circuit refinement:**

* Ensuring gate inputs are at their minimal possible number (no unnecessary inputs).

**Code refinement:**

* Ensuring the variable names are clear and match the circuit.
* Removing redundant parentheses or conditions.

**Documentation refinement:**

* Updating the truth table if any logic changes.
* Save screenshots of the working circuit simulation with test cases.

### **5.5 Final Verification Checklist**

The circuit matches the truth table for all 32 input combinations.  
 Both Python versions give identical outputs for all inputs.  
 Boolean expression matches implemented circuit logic.  
 Documentation (truth table, pseudocode, Boolean expression, flowchart) is consistent.